

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Kwang-Rae CHO

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: July 11, 2003 Examiner: Not Yet Assigned

Title: SEMICONDUCTOR DEVICE WITH CLOCK ENABLE BUFFER TO PRODUCE
STABLE INTERNAL CLOCK SIGNAL

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 11, 2003

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b) (1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P68982US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: New Application FILING DATE: July 11, 2003
 APPLICANT(S): Kwang-Rae CHO TODAY'S DATE: July 11, 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (if Appropriate)
AA	5,880,998	03/09/99	Tanimura et al.	G11C	7/00	10/29/97
AB	6,359,487	03/19/02	Heightley et al.	H03H	11/26	04/03/00

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AC	2-244656	09/28/90	Japan	H01L	21/82	X
AD	2001-44283	02/16/01	Japan	H01L	21/82	X
AE	2001-222887	08/17/01	Japan	G11C	11/407	X
AF	2002-367391	12/20/02	Japan	G11C	19/28	X
AG	11-15783	01/22/99	Japan	G06F	13/42	X
AH	9-198875	07/31/97	Japan	G11C	11/413	X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AI _____

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

Information Disclosure Statement

New U.S. Patent Application for
SEMICONDUCTOR DEVICE WITH CLOCK ENABLE BUFFER TO
PRODUCE STABLE INTERNAL CLOCK SIGNAL

Our Ref. No.: P02H6023/US/iw

Reference No.:

- (1) US Patent No. 5,880,998
- (2) US Patent No. 6,359,487
- (3) JP Laid-Open No. 02-244656
- (4) JP Laid-Open No. 2001-44283
- (5) JP Laid-Open No. 2001-222887
- (6) JP Laid-Open No. 2002-367391
- (7) JP Laid-Open No. 11-15783
- (8) JP Laid-Open No. 09-198875